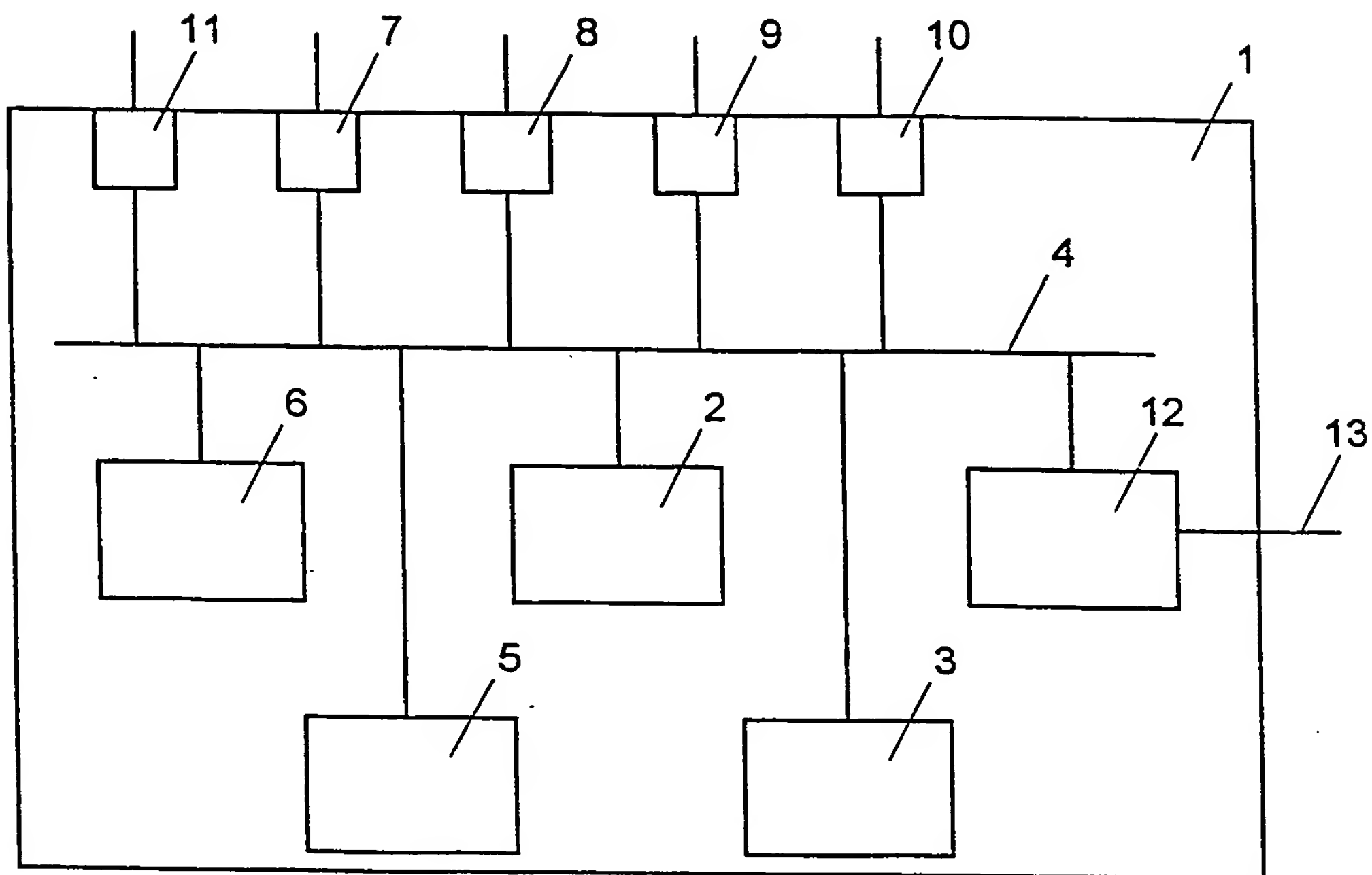


FIG 5



- 1 PRESSURE TRANSDUCER
- 2 MICROPROCESSOR
- 3 MEMORY
- 5-6 MEMORIES
- 7-10 PORTS
- 11 INTERFACE
- 12 SIGNAL MATCHING CIRCUIT